

## عنوان مقاله:

Hybrid domino XOR gate design with low power consumption

## محل انتشار:

دومین کنفرانس بین المللی مهندسی برق (سال: 1396)

تعداد صفحات اصل مقاله: 7

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## خلاصه مقاله:

XOR gates are the fundamental units which are used in many VLSI applications such as adders and microprocessors, and if we can reduce their power consumption and size, it will results in optimized VLSI chips. In this paper, a domino XOR gate in 45nm technology are presented. By eliminating two input inverters and preventing the pulse flow to the output node during the precharge phase, power consumption in this circuit is reduced

## کلمات کلیدی:

low power, domino XOR gate, evaluation phase, precharge phase

## لینک ثابت مقاله در پایگاه سیویلیکا:

<https://civilica.com/doc/698394>

