

عنوان مقاله:

High-Speed Parametric FPGA Implementation of FFT/iFFT Blocks for OFDM Transceivers

محل انتشار:

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تعداد صفحات اصل مقاله: 4

نویسندگان:

Behzad Eghbalkhah - *University of Tehran*

Zhila Amini Sheshdeh - *University of Tarbiat Modares*

Mehdi Ehsani-Nick - *University of Tehran*

خلاصه مقاله:

FFT and iFFT are ones of the most useful blocks in DSP systems. Since the speed of calculation of the result is one of the important factors of this basic block, in this paper we develop a new method for FFT/iFFT implementation which leads to better computation speed in comparison with other common implementation methods

کلمات کلیدی:

FFT, iFFT, implement, FPGA

لینک ثابت مقاله در پایگاه سیویلیکا:

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