

## عنوان مقاله:

Low power pipeline implementation of one dimensional impulse noise reduction median filter

## محل انتشار:

اولین همایش ملی رویکردهای نوین در مهندسی کامپیوتر و بازیابی اطلاعات (سال: 1392)

تعداد صفحات اصل مقاله: 3

## نویسندگان:

Hamid Reza Naji - *Department of Electronic and Computer Engineering, University of Industrial Graduated Studies of Kerman*

Ali Aghasi - *Department of Electronic and Computer Engineering, University of Industrial Graduated Studies of Kerman*

## خلاصه مقاله:

In real time signal processing high speed noise reduction techniques are necessary to deliver proper data in the proper time to the processing core. In addition to the speed of filtering, power consumption can be the other major factor for these kinds of systems specially when considering the increasing usage of portable devices that have to work for a reasonable time by a battery. In this paper we propose a low power high speed architecture to implement one dimensional median filter which highly applicable to reduce impulse noises

## کلمات کلیدی:

Median Filter, Noise Reduction, Pipelining

## لینک ثابت مقاله در پایگاه سیویلیکا:

<https://civilica.com/doc/225568>

